

Applicant : Hajime Kimura et al.
Serial No. : 10/756,756
Filed : January 14, 2004
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Attorney's Docket No.: 12732-207001 / US6910

Amendments to the Drawings:

The attached replacement sheet of drawings includes changes to Figs. 1A and 1B and replaces the original sheet including Fig. 1A and 1B.

In Figures 1A and 1B, the reference number "130" has been changed to "131".

Attachments following last page of this Amendment:

Replacement Sheet (1 page)
Annotated Sheet Showing Change(s) (1 page)

REMARKS

Claims 1-78 and 80 are pending in the application, with claims 1-12 and 19 being independent. Claim 79 has been canceled and claims 1-12 and 19 have been amended for clarity. The amendments find support in Fig. 1 (claims 1 and 19), Fig. 2 (claim 2), Fig. 5B (claim 3), Fig. 5A (claim 4), Fig. 6A (claims 5 and 6), Fig. 7B (claim 7), Fig. 7A (claim 8), Fig. 8 (claims 9 and 10) and Fig. 9 (claims 11 and 12). The abstract and Figs. 1A and 1B have been amended in response to the Examiner's objections. No new matter has been introduced.

Claim 3 has been amended and claim 79 has been canceled in view of the Examiner's objections to those claims.

The amendments to claim 19 are believed to address the rejection under section 112, second paragraph. In particular, with respect to the rejection, claim 19 has been amended to change "each certain period" to "a certain period."

Claims 1, 2, 19 and 24-26 have been rejected as being anticipated by Ishizuka (U.S. Patent No. 5,923,309).

With respect to claims 1 and 2, applicant requests reconsideration and withdrawal of this rejection because Ishizuka does not describe or suggest a changing over means that is electrically connected to the current sources and output lines, and that selects one of the current sources to be electrically connected to each of the output lines, as recited in claims 1 and 2. For example, Fig. 8 of Ishizuka, which the rejection asserts illustrates the changing over means in the form of switches SW₀ to SW₃, shows that a separate set of current sources is provided for each of the output lines, such that none of the current sources may be connected to each of the output lines.

With respect to claim 19, applicant requests reconsideration and withdrawal of this rejection because Ishizuka does not describe or suggest changing an electrical connection between a first signal line and the current source to an electrical connection between a second signal line and the current source after a certain period, as recited in claim 19. In particular, as noted above, Ishizuka appears to show that each of the current sources may be connected to only a single output line, with the selection provided by Ishizuka being with respect to which of a set of current sources is connected to an associated output line.

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Claims 3-18, 20-23 and 27-80 have been rejected as being unpatentable over Ishizuka in view of Yamada (U.S. Patent No. 5,990,629). Like independent claims 1 and 2, each of independent claims 3-12 has been amended to recite a changing over means that is electrically connected to current sources and lines, and that selects a current source to be electrically connected to each of the lines. Accordingly, applicant requests reconsideration and withdrawal of this rejection for the reasons presented above and because Yamada, which is cited as showing a shift register and latch circuitry, does not remedy the failure of Ishizuka to describe or suggest the subject matter of the independent claims.

Applicant submits that all claims are in condition for allowance.

Enclosed is a \$120 check for the Petition for Extension of Time fee. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: 10/27/05

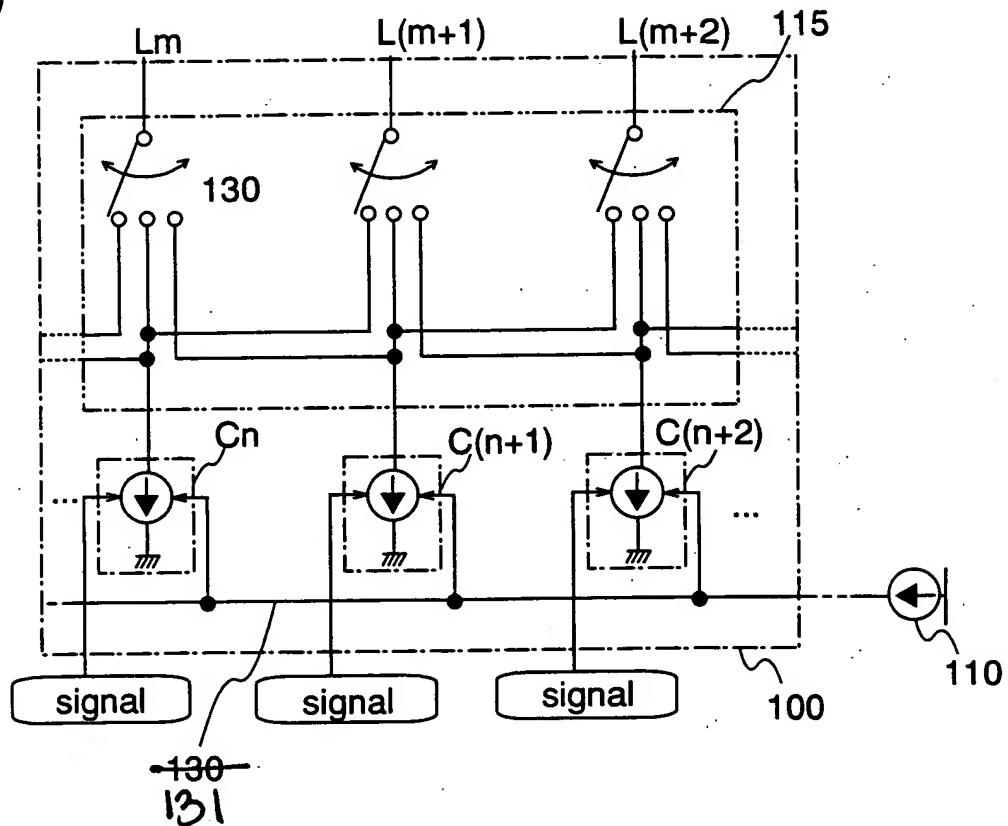

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FIG. 1

(A)



(B)

